PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the Application of

Attn: OIPE

Fumiaki KARASAWA et al.

Application No.:

10/642,737

Docket No.:

116884

Filed: August 19, 2003

For:

SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING THE SAME,

CIRCUIT BOARD AND ELECTRONIC APPARATUS

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

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JAO:JSK/kap

Date: May 19, 2004

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08/19/2003 10/642,737

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APR 2 6 2004 **OLIFF & BERRIDGE** **CONFIRMATION NO. 3798**

FILING RECEIPT

OC000000012416934

Date Mailed: 04/22/2004

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Applicant(s)

Ina

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Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-241827 08/22/2002

If Required, Foreign Filing License Granted: 04/21/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor apparatus and the manufacturing method, and circuit board and electronic device

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